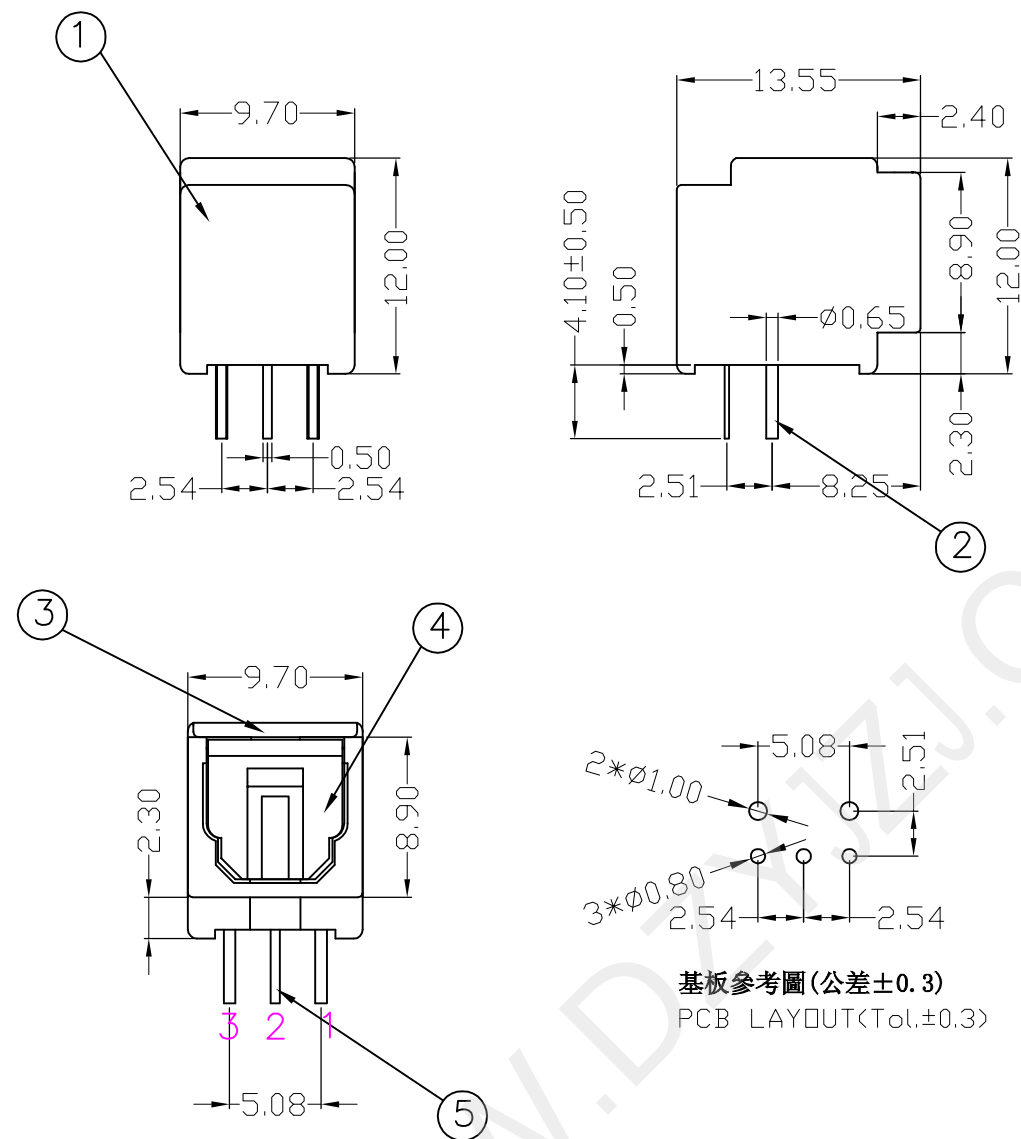




基板参考圖(公差±0.3)
PCB LAYOUT(Tol.±0.3)

Pin Function:

1. Vout
2. GND
3. Vcc

NO	PART NAME	QTY	MATERIAL	REMARK
1	BODY	1	PBT	
2	PIN	2	銅包鋼線	
3	Spring	1	SUS304	
4	SHUTTER	1	PBT	
5	IR	1		RECEIVING



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TOLERANCE: X.X ±0.30 X.XX ±0.25 X.XXX ±0.15 X.' ±2" X.X' ±0.5'  UNIT: mm [inch] SCALE:1:1 SIZE: A4	DRAWN BY : 陈一鸣	DATE : 2014-02-23	PART NAME: 光纤插座 接收	
	CHECKED BY: 马跃	DATE : 2014-02-23	PART NO.	HYC-DLR1150
	APPROVED BY: 邱敏	DATE : 2014-02-23	MOLD NO.	
			DRAW NO:	HYC-2305221557
			SHEET NO.	1 OF 1