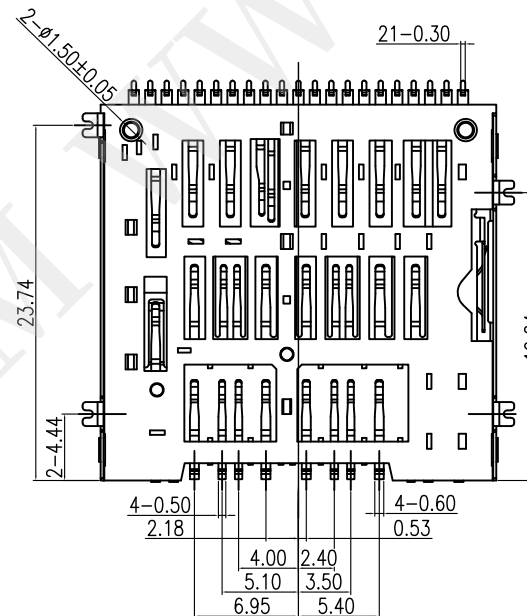
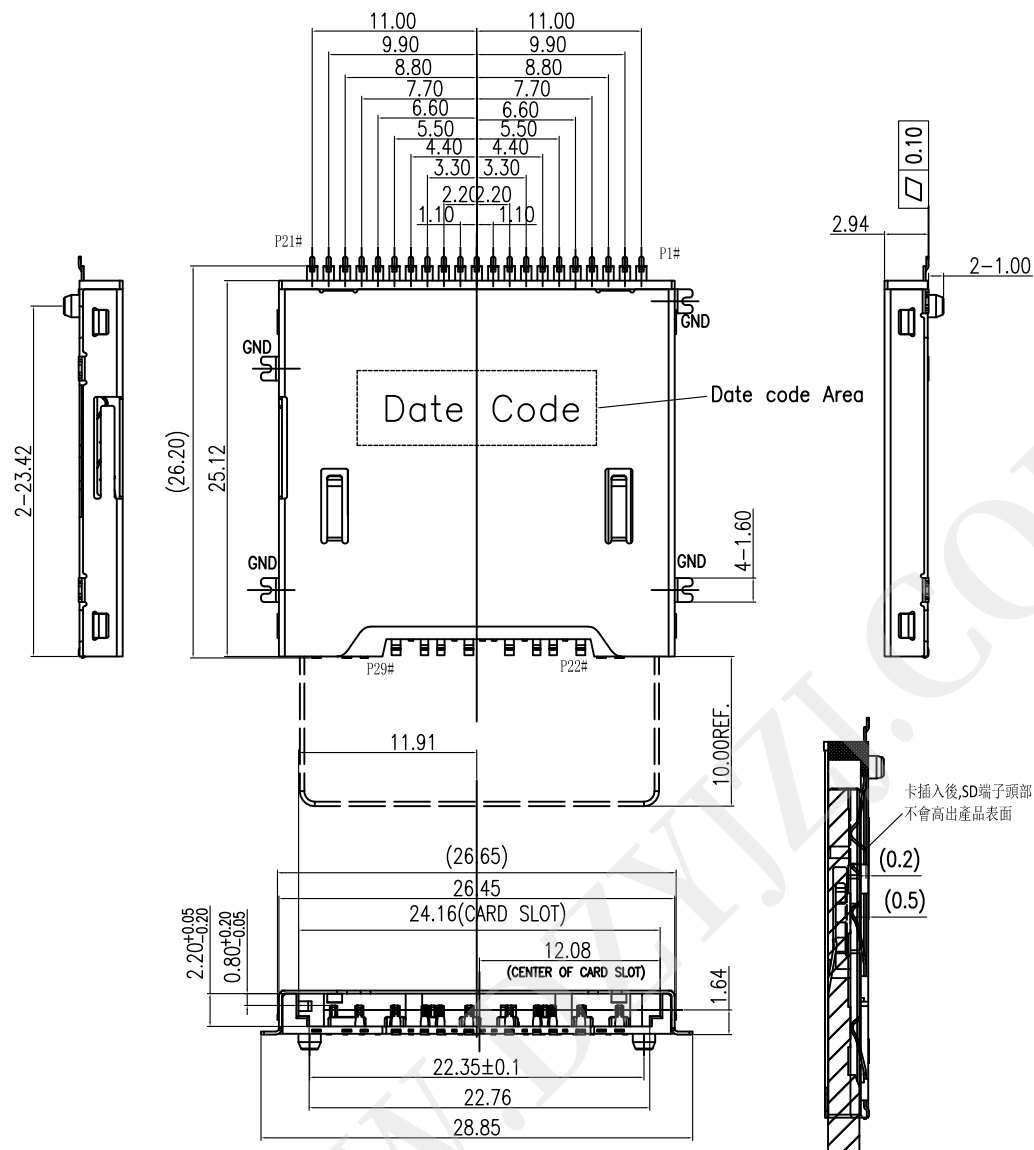
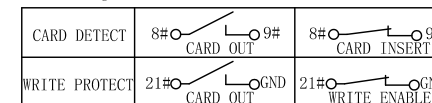


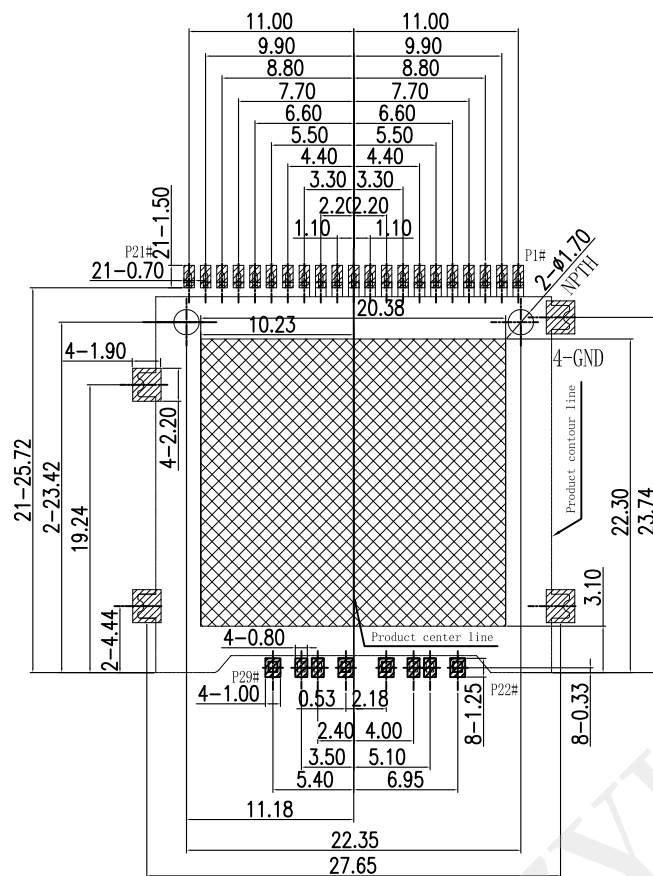


Circuit Diagram for CARD Detect/Write Protect Switch



深圳市华宇创精密电子有限公司

|                                                                           |                    |                      |                                        |
|---------------------------------------------------------------------------|--------------------|----------------------|----------------------------------------|
| TOLERANCE:<br>X.X ±0.30<br>X.XX ±0.25<br>X.XXX ±0.15<br>X' ±2" X.X' ±0.5" | DRAWN BY :<br>陈一鸣  | DATE :<br>2014-02-23 | PART NAME:<br>SD 8.0 NO PUSH SMT 2.94H |
|                                                                           | CHECKED BY:<br>马跃  | DATE :<br>2014-02-23 | PART NO.<br>HYC98-SD29-294             |
| <br>UNIT: mm [inch]<br>SCALE:1:1 SIZE: A4                                 | APPROVED BY:<br>邱敏 | DATE :<br>2014-02-23 | DRAW NO:<br>HYC-2509011530             |
|                                                                           |                    |                      | SHEET NO.<br>1 OF 1                    |



RECOMMEND P.C.B LAYOUT  
(General tolerance  $\pm 0.05$ )

▨ PAD AREA  
▩ NO COPPER AREA  
(NO Trace&Via&GND)

| Pin Assignment |        |           |        |                                                              |
|----------------|--------|-----------|--------|--------------------------------------------------------------|
| No.            | PIN#.  | Name      | Type   | Description                                                  |
| P1             | PIN10# | VSS3      | S      | Supply voltage ground                                        |
| P2             | PIN19# | VDD1a     | S      | Supply voltage (3.3V)                                        |
| P3             | PIN9#  | CLKREQ#   | I/O/OD | Reference clock request signal.Also used by LI PM sub-states |
| P4             | PIN11# | PCIe Tx0+ | IDS    | Card Input; lane 0                                           |
| P5             | PIN1#  | PERST#    | I      | PE-Reset is a functional reset                               |
| P6             | PIN12# | PCIe Tx0- | IDS    | Card Input; lane 0                                           |
| P7             | PIN2#  |           |        | Not Used                                                     |
| P8             | CD PIN | CD        |        |                                                              |
| P9             | PIN3#  | VSS1      | S      | Supply voltage ground                                        |
| P10            | PIN13# | VSS4      | S      | Supply voltage ground                                        |
| P11            | PIN14# | VDD2      | S      | Supply voltage (1.8V)                                        |
| P12            | PIN4#  | VDD1      | S      | Supply voltage (3.3V)                                        |
| P13            | PIN15# | PCIe Rx0- | ODS    | Card Output; lane 0                                          |
| P14            | PIN5#  |           |        | Not Used                                                     |
| P15            | PIN16# | PCIe Rx0+ | ODS    | Card Output; lane 0                                          |
| P16            | PIN6#  | VSS2      | S      | Supply voltage ground                                        |
| P17            | PIN17# | VSS5      | S      | Supply voltage ground                                        |
| P18            | PIN7#  | REFCLK+   | IDS    | PCIe Ref Clock                                               |
| P19            | PIN18# | VDD3      | S      | Supply voltage (1.2V)                                        |
| P20            | PIN8#  | REFCLK-   | IDS    | PCIe Ref Clock                                               |
| P21            | WP PIN | WP        |        |                                                              |
| P22            | PIN20# | VSS6      | S      | Supply voltage ground                                        |
| P23            | PIN21# | PCIe Tx1+ | IDS    | Card Input; lane 1                                           |
| P24            | PIN22# | PCIe Tx1- | IDS    | Card Input; lane 1                                           |
| P25            | PIN23# | VSS7      | S      | Supply voltage ground                                        |
| P26            | PIN24# | VDD2a     | S      | Supply voltage (1.8V)                                        |
| P27            | PIN25# | PCIe Rx1- | ODS    | Card Output; lane 1                                          |
| P28            | PIN26# | PCIe Rx1+ | ODS    | Card Output; lane 1                                          |
| P29            | PIN27# | VSS8      | S      | Supply voltage ground                                        |

Notes:

1.Material:

Housing: LCP S475 Black

Terminal:C5210

Shell: SUS304-H

2.Plating:

Terminal:30u" Au Plated On Contact Area And

G/F Plated On Solder Area

Sheet:G/F Plated On Solder Area

3.Operation Voltage: 10V AC

Current Rating: 1.0A MAX

Contact Resistance:100m MAX.

Dielectric withstanding voltage: 500V DC/1minute

Insulation Resistance: 1000M Minutes. 500V DC

Operation Temperature: -20 ° +60

Storage Temperature: -20 ° +70

IR Reflow 260°C 5s



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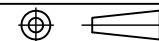
TOLERANCE:

X.X  $\pm 0.30$

X.XX  $\pm 0.25$

X.XXX  $\pm 0.15$

X.'  $\pm 2$  X.X'  $\pm 0.5$



UNIT: mm [inch]

SCALE:1:1 SIZE: A4

DRAWN BY :

陈一鸣

DATE :

2014-02-23

PART NAME:

SD 8.0 NO PUSH SMT 2.94H

CHECKED BY:

马跃

DATE :

2014-02-23

APPROVED BY:

邱敏

DATE :

2014-02-23

PART NO.

HVC98-SD29-294

MOLD NO.

HVC-2509011530

DRAW NO:

HVC-2509011530

SHEET NO.

1 OF 1